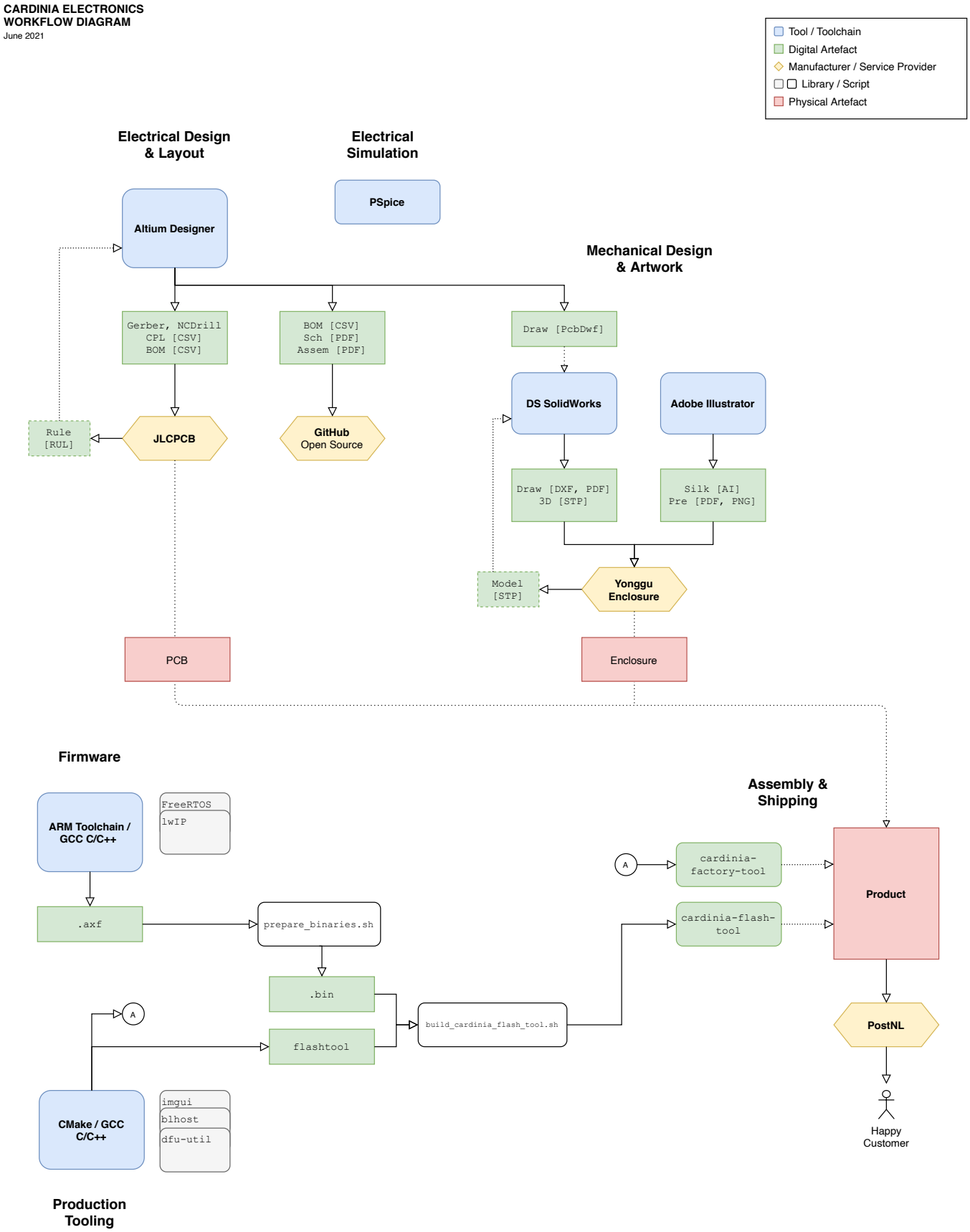
CARDINIA ELECTRONICS
WORKFLOW DIAGRAM
June 2021
Tool / Toolchain
Digital Artefact
Manufacturer / Service Provider
Library / Script
Physical Artefact
Electrical Design & Layout
Electrical Simulation
PSpice
Altium Designer
Mechanical Design & Artwork
Gerber, NCDrill
CPL [CSV]
BOM [CSV]
BOM [CSV]
Sch [PDF]
Assem [PDF]
Draw [PcbDwf]
DS SolidWorks
Adobe Illustrator
Rule [RUL]
JLCPCB
GitHub Open Source
Draw [DXF, PDF]
3D [STP]
Silk [AI]
Pre [PDF, PNG]
Model [STP]
Yonggu Enclosure
PCB
Enclosure
Firmware
Assembly & Shipping
ARM Toolchain / GCC C/C++
FreeRTOS
lwIP
A
cardinia-factory-tool
Product
.axf
prepare_binaries.sh
cardinia-flash-tool
.bin
build_cardinia_flash_tool.sh
PostNL
A
flashtool
CMake / GCC C/C++
imgui
blhost
dfu-util
Happy Customer
Production Tooling
Website
nginx
vue.js
node.js
express.js
Docker
dockerimage
DigitalOcean